



US009324606B2

(12) **United States Patent**
Chi et al.

(10) **Patent No.:** **US 9,324,606 B2**
(45) **Date of Patent:** **Apr. 26, 2016**

(54) **SELF-ALIGNED REPAIRING PROCESS FOR BARRIER LAYER**

(2013.01); **H01L 21/76876** (2013.01); **H01L 23/5329** (2013.01); **H01L 23/53252** (2013.01); **H01L 23/53266** (2013.01); **H01L 2924/0002** (2013.01)

(71) Applicant: **TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY, LTD.**, Hsinchu (TW)

(58) **Field of Classification Search**
CPC **H01L 21/76877**; **H01L 21/32051**; **H01L 23/53225**
USPC **438/643, 644, 645, 680**
See application file for complete search history.

(72) Inventors: **Chih-Chien Chi**, Hsinchu (TW);
Chung-Chi Ko, Nantou (TW);
Mei-Ling Chen, Kaohsiung (TW);
Huang-Yi Huang, Hsinchu (TW);
Szu-Ping Tung, Taipei (TW);
Ching-Hua Hsieh, Hsinchu (TW)

(56) **References Cited**

U.S. PATENT DOCUMENTS

(73) Assignee: **TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.**, Hsinchu (TW)

8,268,397 B2 * 9/2012 Cho et al. 427/250
2008/0045013 A1 * 2/2008 Lavoie et al. 438/687
2015/0111316 A1 * 4/2015 Koschinsky et al. 438/14

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 40 days.

* cited by examiner

Primary Examiner — Quoc Hoang

(21) Appl. No.: **14/151,711**

(74) *Attorney, Agent, or Firm* — McDermott Will & Emery LLP

(22) Filed: **Jan. 9, 2014**

(57) **ABSTRACT**

(65) **Prior Publication Data**

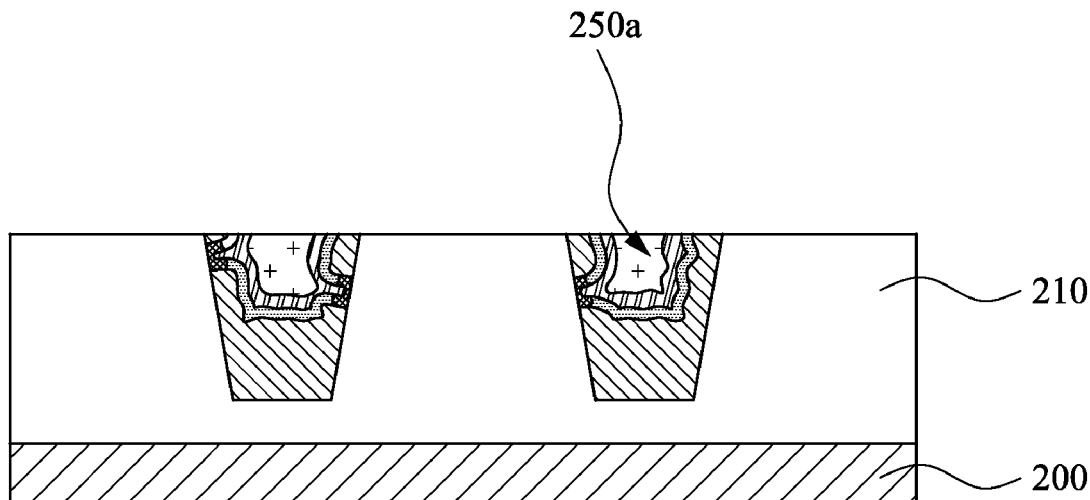
US 2015/0194343 A1 Jul. 9, 2015

(51) **Int. Cl.**
H01L 21/4763 (2006.01)
H01L 21/768 (2006.01)
H01L 23/532 (2006.01)

A self-aligned repairing process for a barrier layer is provided. A repair layer is formed by chemical vapor deposition using an organometallic compound as a precursor gas. The precursor gas adsorbed on a dielectric layer exposed by defects in a barrier layer is transformed to an insulating metal oxide layer, and the precursor gas adsorbed on the barrier layer is transformed to a metal layer.

(52) **U.S. Cl.**
CPC **H01L 21/76843** (2013.01); **H01L 21/76807**

20 Claims, 5 Drawing Sheets



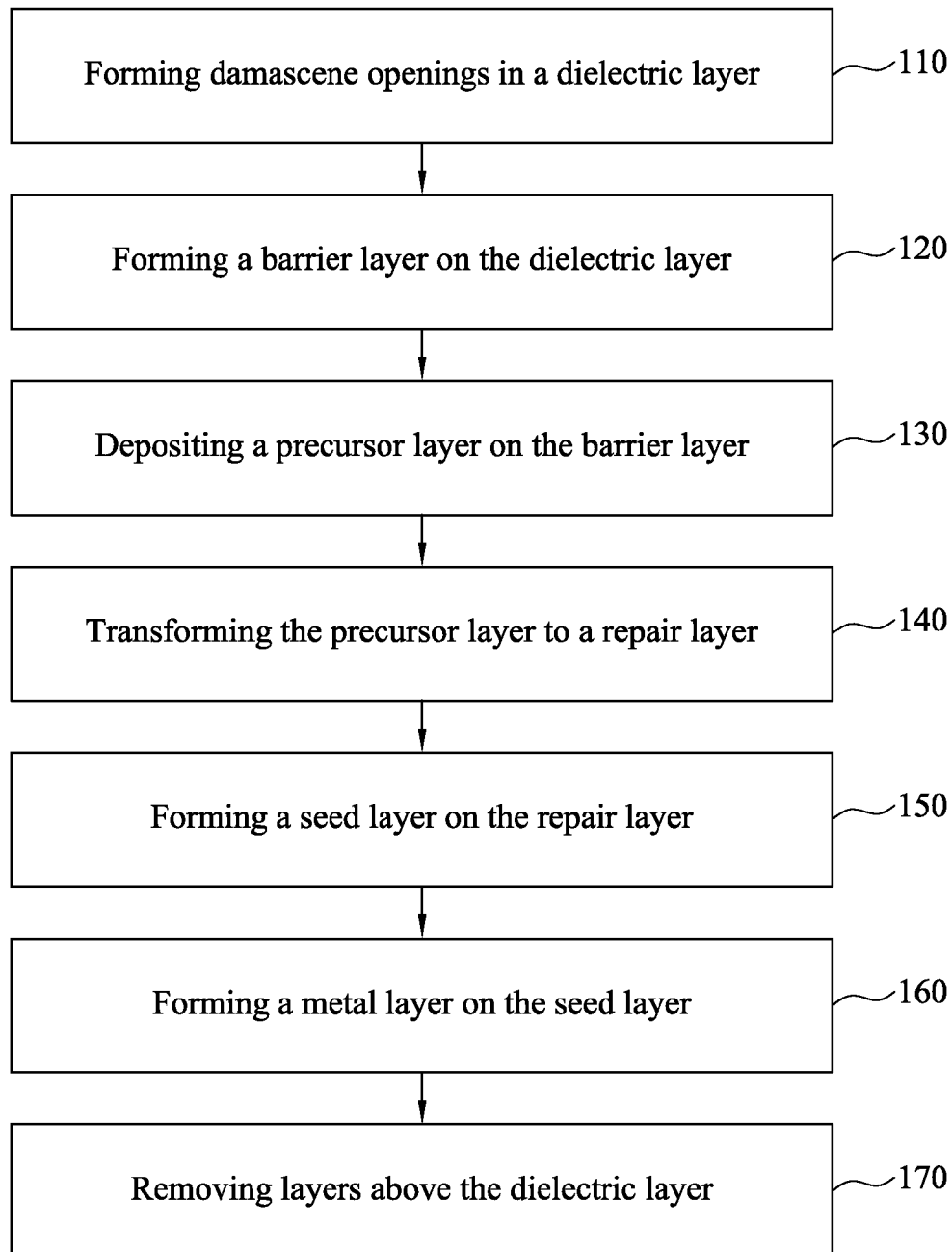


Fig. 1

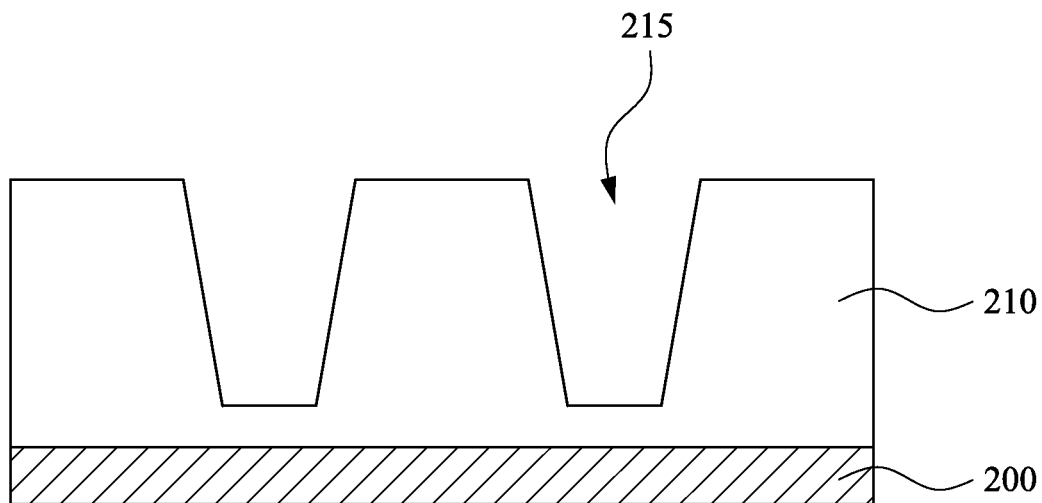


Fig. 2A

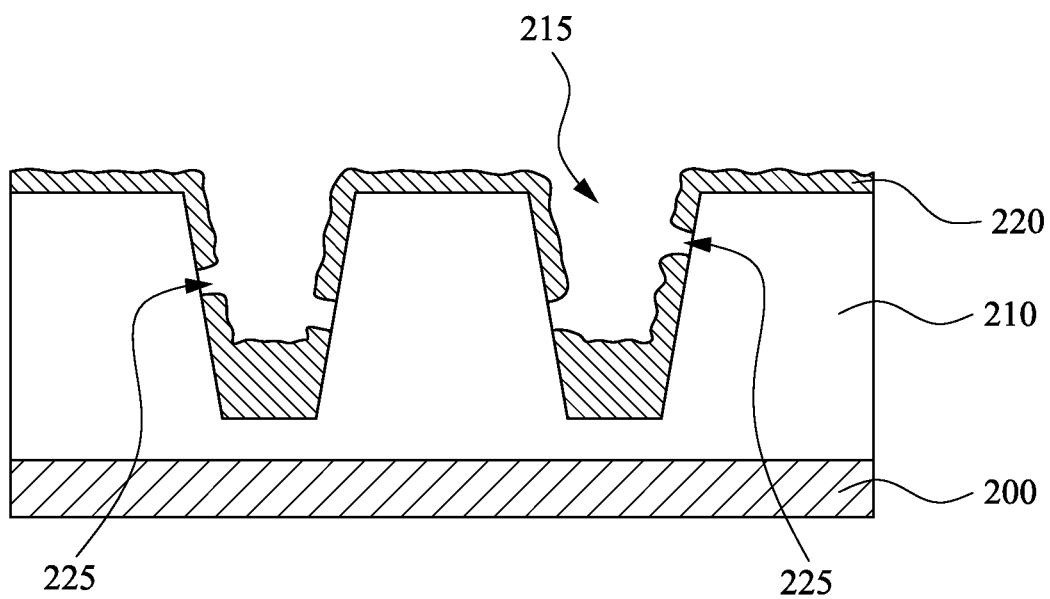


Fig. 2B

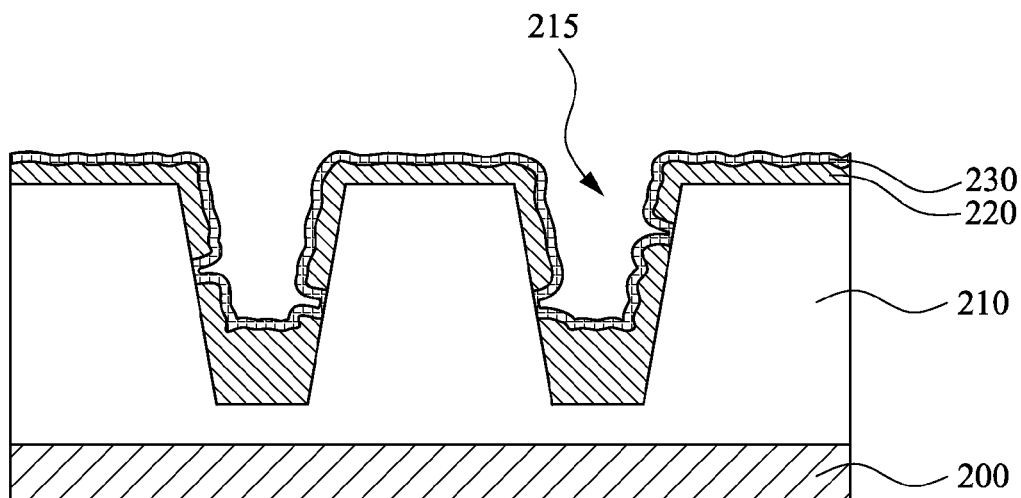


Fig. 2C

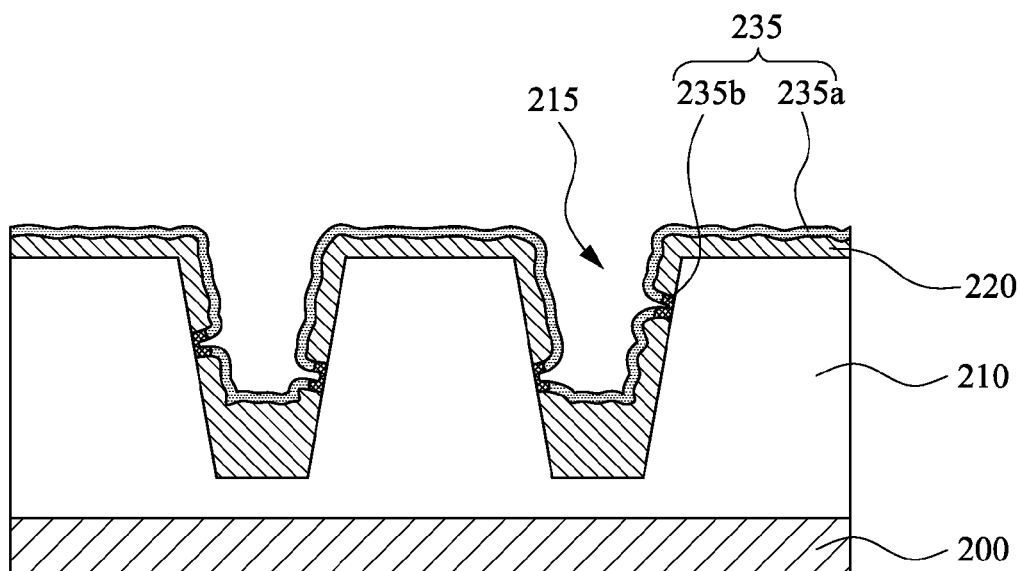


Fig. 2D

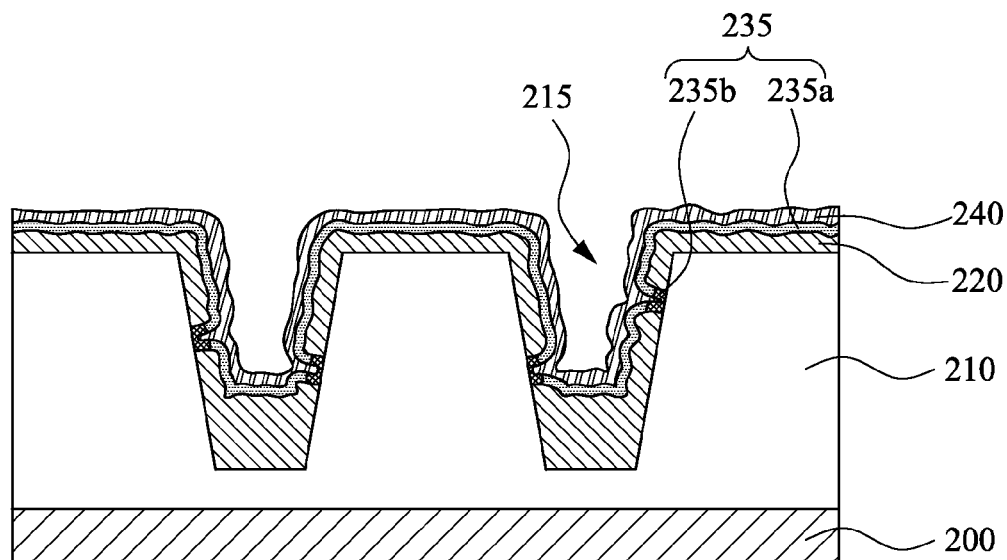


Fig. 2E

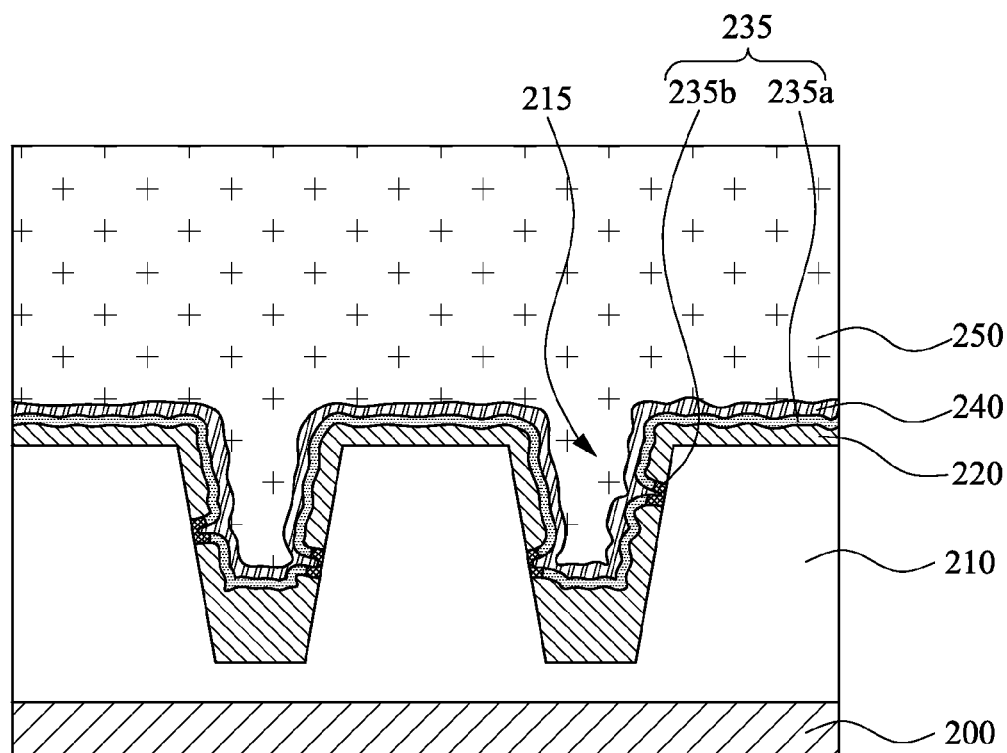


Fig. 2F

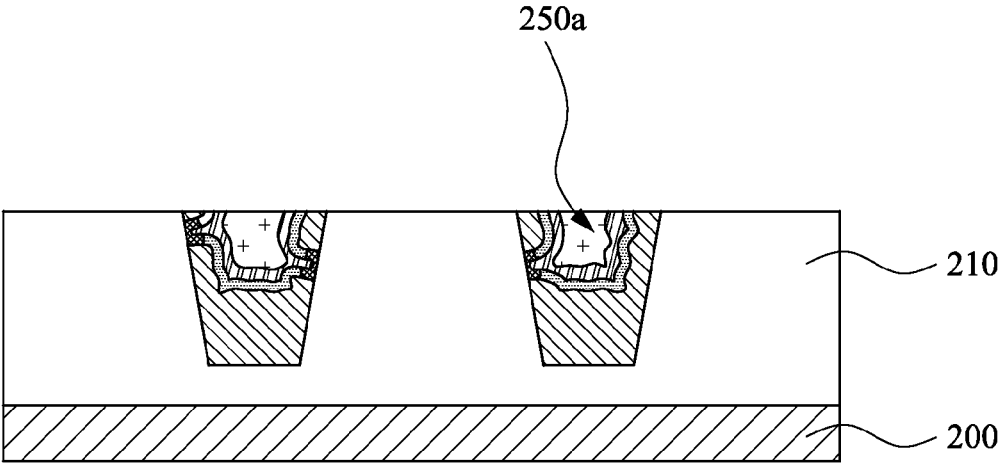


Fig. 2G

SELF-ALIGNED REPAIRING PROCESS FOR BARRIER LAYER

BACKGROUND

As the design rule for integrated circuits is continuously scaled down, the dimension of damascene opening is continuously reduced. Therefore, the step coverage of a barrier layer covering the inner surface of the damascene opening is getting worse, and defects may be formed in the barrier layer. For example, if 30-50 Å of barrier layer is blanket deposited on a wafer, the thickness of the barrier layer on sidewalls of openings, including vias and trenches, may be less than 5-10 Å. Therefore, defects can be easily formed in the barrier layer located on sidewalls of openings.

Since barrier layer is used to surrounding the later formed metal interconnect to prevent metal from diffusing into the dielectric layer where the damascene opening is located, these defects in the barrier layer provide passages for the metal diffusion. Moreover, the low-k dielectric layer mostly made from porous dielectric materials make the problem of metal diffusion through defects in the barrier layer more serious, since the larger the total pore volume of the porous dielectric material has, the lower the dielectric constant of the porous dielectric material has.

BRIEF DESCRIPTION OF THE DRAWINGS

Aspects of the present disclosure are best understood from the following detailed description when read with the accompanying figures. It is noted that, in accordance with the standard practice in the industry, various features are not drawn to scale. In fact, the dimensions of the various features may be arbitrarily increased or reduced for clarity of discussion.

FIG. 1 is a flowchart of a self-aligned repairing process for a barrier layer according some embodiments of this disclosure.

FIGS. 2A-2G are cross-sectional diagrams showing a self-aligned repairing process for a barrier layer according some embodiments of this disclosure.

The drawings, schematics and diagrams are illustrative and not intended to be limiting, but are examples of embodiments of the disclosure, are simplified for explanatory purposes, and are not drawn to scale.

DETAILED DESCRIPTION

The following disclosure provides many different embodiments, or examples, for implementing different features of the invention. Specific examples of components and arrangements are described below to simplify the present disclosure. These are, of course, merely examples and are not intended to be limiting. For example, the formation of a first feature over or on a second feature in the description that follows may include embodiments in which the first and second features are formed in direct contact, and may also include embodiments in which additional features may be formed between the first and second features, such that the first and second features may not be in direct contact. In addition, the present disclosure may repeat reference numerals and/or letters in the various examples. This repetition is for the purpose of simplicity and clarity and does not in itself dictate a relationship between the various embodiments and/or configurations discussed.

Further, spatially relative terms, such as "beneath," "below," "lower," "above," "upper" and the like, may be used herein for ease of description to describe one element or

feature's relationship to another element(s) or feature(s) as illustrated in the figures. The spatially relative terms are intended to encompass different orientations of the device in use or operation in addition to the orientation depicted in the figures. The apparatus may be otherwise oriented (rotated 90 degrees or at other orientations) and the spatially relative descriptors used herein may likewise be interpreted accordingly.

According to some embodiments, a self-aligned repairing process for a barrier layer is provided. A damascene opening is formed in a dielectric layer on a substrate, and the surface of the dielectric layer has free hydroxyl groups. A barrier layer is then on the dielectric layer to cover inner surfaces of the damascene opening. A precursor layer is formed on the barrier layer by chemical vapor deposition using an organo-metallic compound containing a central metal atom as a precursor gas, wherein the central metal atom can be Al, Ru, Co, Mn, or Ta, for example. Finally, the precursor layer is transformed to a repair layer. The precursor layer on the dielectric layer exposed by defects in the barrier layer is transformed to an insulating metal oxide layer, and the precursor layer on the barrier layer is transformed to a metal layer.

According to some other embodiments, a method of forming a damascene structure is provided. A damascene opening is formed in a dielectric layer on a substrate, and the surface of the dielectric layer has free hydroxyl groups. A barrier layer is then on the dielectric layer to cover inner surfaces of the damascene opening. A repair layer is deposited on the barrier layer by chemical vapor deposition using an organo-metallic compound containing a central metal atom as a precursor gas. The central metal atom can be Al, Ru, Co, Mn, or Ta, for example. The precursor gas adsorbed on the dielectric layer exposed by defects in the barrier layer is transformed to an insulating metal oxide layer, and the precursor gas adsorbed on the barrier layer is transformed to a metal layer. Next, a seed layer is deposited on the repair layer. A metal layer is formed on the seed layer to fill in the damascene opening. An upper portion of the metal layer, the seed layer, the repair layer, the barrier layer, and the dielectric layer is then removed to form a metal interconnect in the damascene opening.

According to some other embodiments, a damascene structure is also provided. In the damascene structure, a dielectric layer is disposed on a substrate, wherein the dielectric layer has a damascene opening. A barrier layer is disposed on inner surface of the damascene opening. A repair layer is disposed on the barrier layer, wherein the repair layer disposed on the barrier layer is composed of a metal, and the repair layer disposed on the dielectric layer exposed by defects in the barrier layer is composed of insulating metal oxide. A seed layer is disposed on the repair layer. A metal interconnect is disposed in the damascene opening.

FIG. 1 is a flowchart of a self-aligned repairing process for a barrier layer according some embodiments of this disclosure. FIGS. 2A-2G are cross-sectional diagrams showing a self-aligned repairing process for a barrier layer according some embodiments of this disclosure. FIGS. 1 and 2A-2G are referred below at the same time.

In step 110 of FIG. 1 and FIG. 2A, damascene openings 215 are formed in a dielectric layer 210 on a substrate 200. The dielectric layer 210 can be made from a low-k dielectric material having some free hydroxyl (—OH) groups on the surface thereof. The low-k dielectric material is defined to be a dielectric material have a dielectric constant lower than the dielectric constant of silicon dioxide. Common low-k dielectric material includes fluorine-doped silicon dioxide, carbon-doped silicon dioxide, porous silicon dioxide, porous carbon-

The chemical vapor deposition (CVD) above can be thermal CVD, plasma-enhanced CVD, metal-organic CVD, atomic layer deposition (ALD), thermal ALD, or plasma-enhanced ALD, for example.

4. The method of claim 1, wherein the organometallic compound is tricarbonyl[(1,2,3,4-eta)-1,3-cyclohexadiene]ruthenium, triruthenium dodecacarbonyl, bis(ethylcyclopent-

5

tadienyl) ruthenium, bis(methylcyclopentadienyl) ruthenium, or tris(acetylacetonate) ruthenium.

5. The method of claim 1, wherein the organometallic compound is cobalt dicarbonyl cyclopentadiene, or dicobalt hexacarbonyl tert-butylacetylene.

6. The method of claim 1, wherein the organometallic compound is dimanganese decacarbonyl.

7. The method of claim 1, wherein the organometallic compound is pentakis(dimethylamino) tantalum, (tert-butylimido) tris(ethylmethylamido) tantalum, tris(diethylamino) (tert-butylimido) tantalum, or (tert-amylimido) tris(dimethylamido) tantalum.

8. The method of claim 1, wherein the chemical vapor deposition is plasma enhanced chemical vapor deposition, metalorganic chemical vapor deposition, or plasma enhanced atomic layer deposition.

9. The method of claim 1, wherein forming the damascene opening is performed by photolithography or etching.

10. A method of forming a damascene structure, comprising:

forming a damascene opening in a dielectric layer on a substrate, wherein a surface of the dielectric layer has free hydroxyl groups;

forming a barrier layer on the dielectric layer to cover inner surfaces of the damascene opening;

depositing a repair layer on the barrier layer by chemical vapor deposition using an organometallic compound containing a central metal atom as a precursor gas, wherein the central metal atom is Al, Ru, Co, Mn, or Ta, and wherein the precursor gas adsorbed on the dielectric layer exposed by defects in the barrier layer is transformed to an insulating metal oxide portion, and the precursor gas adsorbed on the barrier layer is transformed to a metal portion;

forming a seed layer on the repair layer;

forming a metal layer on the seed layer to fill in the damascene opening; and

6

removing an upper portion of the metal layer, the seed layer, the repair layer, the barrier layer, and the dielectric layer to form a metal interconnect in the damascene opening.

11. The method of claim 10, wherein the barrier layer is made from a metal containing Co, Ru, Ta, or a combination thereof, or a conductive ceramics, which is tantalum nitride, indium oxide, copper silicide, tungsten nitride, or titanium nitride.

12. The method of claim 10, wherein the organometallic compound is $\text{Al}(\text{CH}_3)_3$ or $\text{Al}(\text{C}_2\text{H}_5)_3$.

13. The method of claim 10, wherein the organometallic compound is (tricarbonyl[(1,2,3,4-eta)-1,3,-cyclohexadiene] ruthenium), triruthenium dodecacarbonyl, bis(ethylcyclopentadienyl) ruthenium, bis(methylcyclopentadienyl) ruthenium, or tris(acetylacetonate)ruthenium.

14. The method of claim 10, wherein the organometallic compound is cobalt dicarbonyl cyclopentadiene, or dicobalt hexacarbonyl tert-butylacetylene.

15. The method of claim 10, wherein the organometallic compound is $\text{Mn}_2(\text{CO})_{10}$.

16. The method of claim 10, wherein the organometallic compound is pentakis(dimethylamino) tantalum, (tert-butylimido) tris(ethylmethylamido) tantalum, tris(diethylamino) (tert-butylimido) tantalum, or (tert-amylimido) tris(dimethylamido) tantalum.

17. The method of claim 10, wherein the chemical vapor deposition is plasma enhanced chemical vapor deposition, metalorganic chemical vapor deposition, or plasma enhanced atomic layer deposition.

18. The method of claim 10, wherein the seed layer is made from Cu, Co, Al, Ag, or any combinations thereof.

19. The method of claim 10, wherein the metal layer made from copper.

20. The method of claim 10, wherein forming the seed layer is performed by sputtering or evaporation.

* * * * *